Utility Patent Application

for

"Optical Logic Gates Using Semiconductor Optical Amplifiers"

by

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CROSS REFERENCE TO RELATED APPLICATIONS

This application claims the benefit of priority from Provisional Patent

Application Serial No. 60/393,928 filed July 3, 2002 entitled "All Optical Logic

Gates Using Semiconductor Optical Amplifiers", currently co-pending.

10   It is expensive in telecom system to switch back from high speed optics

to electronics because of the need to demultiplex hundreds of channels to rates

that the electronics can handle and then multiplex them back into optics after

processing. Further, the expensive electronics must be replaced for each new

generation of faster telecom equipment. Therefore, processing by all-optical

15   logic [5] is a long term goal for the telecom industry.   The logic must operate

at over 2.5 Gbps. All optical logic has the important advantage that the links can become transparent to bit rate, protocol, frequency etc. This allows cost effective expansion to the next faster generation of telecom equipment.

Previously we proposed an all optical design for a bit serial ripple carry adder consisting of full adders and using semiconductor optical amplifiers in cross gain modulation as all-optical logic elements [4]. The bits were assigned different frequencies to allow parallel computation and to take advantage of the wavelength division multiplexing technology developed for the telecom industry. The term all-optical is used to described processing in which all signal paths are optical whether used for control or information. Semiconductor optical amplifiers (SOAs) can perform all optical logic because they have nonlinearity, low latency, and require low power.

In this paper we investigate more closely the performance of SOAs as all-optical logic NOR (not OR) and NXOR (not exclusive OR) gates using more exact device models and operating at 2.5 Gbps with return to zero (RZ) signals. The truth table for OR, NOR, XOR and NXPR gates is describe in Table 1.

In section 2, we show simulation results characterizing SOAs and describe briefly the principle of cross-gain modulation (XGM). In section 3 we

show simulation results for all-optical NOR gates. Results for simulating all-optical NXOR gates are shown in section 4.

2.   SEMICONDUCTOR OPTICAL AMPLIFIER CHARACTERISTICS

Characteristics for an SOA depend on the type of SOA, for example, a

5   multiple quantum well device (MQW) has different properties from a bulk device.

2.1   Characteristics of SOAs

Semiconductor optical amplifiers (SOAs) are similar to laser diodes but have minimal reflective facets so that most of the light passes only once

10   through the SOA. Figure 1 (a) shows the input-output characteristics of the multiple quantum well (MQW) device semiconductor optical amplifiers (SOAs) used for three different bias currents, 50mA, 100mA and 150mA. A light wavelength 1550nm or a corresponding light frequency 193.1THz (10^{12}Hz) was used. As the device saturates, the output no longer increases at the same rate

15   due to gain saturation. The output saturation power at which the gain has fallen by 3dB from the peak (i,e the half power point) is an important parameter

of the SOA. Gain saturation is shown as a function of input power in figure 1(b).

2.2   Principle of cross gain modulation

Cross-gain modulation (XGM) is semiconductor optical amplifiers has been investigated extensively for wavelength shifting,[3],[1],[9] and has been used for NOR gates [6] and digital information processing [4]. Wavelength converters have been operated at 20 Gbps [8] and signal and probe levels for optimum performance have been studied [2], [7]. The principle relies on the fact that the gain of a semiconductor optical amplifier falls off with increasing input power, figure 1(b). This permits cross coupling between inputs that have different frequencies. A modulated pump signal at the XGM-SOA input causes it to go in and out of saturation according to the bit status '1' or '0'. A continuous wave probe at a different wavelength from the pump is also supplied to the XGM-SOA in a co-propagating or counter-propagating connection. The probe is modulated by the gain. The signal has been transferred from the pump wavelength at the input to the XGM-SOA to the probe wavelength at the XGM-SOA output. This is known as frequency shifting or frequency conversion. The performance depends on the wavelengths, signal levels and bit rates. Note that

the output modulation is the inverse or complement of the input modulation for a single XGM stage. For this reason a single stage performs a NOT logic operation while moving the signal from one frequency to another. In fact by connecting two signals into the input, an SOA in XGM can perform a NOR operation in a manner similar to a transistor. Next we show simulation results for NOR and NXOR logic gates.

3.   NOR GATE USING SOAS IN CROSS-GAIN MODULATION

Figure 2 shows a schematic for a NOR gate using an SOA in cross gain modulation. The NOR gate inputs are two 2.5Gbps RZ bit streams. The signals at inputs A and B have frequencies of 193.1 THz and 193 THz respectively. Sample bit streams from a random number generator are shown for the upper (A) and lower (B) inputs in figure 3(a) and 3(b) respectively. The probe laser provides continuous wave (CW) light at 193.2 THz. This passes through the SOA and is modulated by the changing gain. The output is the inverse of the OR operation or a NOR of the two inputs at A and B. Note that, regardless of whether the inputs are noisy, wideband or have unknown frequencies, the output has been placed on a clean new frequency at 193.2 THz. The SOA must saturate when a one appears at either input so that when a one level

appears at both inputs simultaneously, there is no change in output, as required for cascadable all-optical logic gates. A band pass filter at the output passes only the 193.2 frequency. This filter is required when using co-propagation of input signals and probe.

5   The combined inputs $A + B$ at the output of the first x-coupler in figure 2 is shown in figure 4 and this signal will cause gain compression due to device saturation in the SOA. Comparisons with figure 3(a) and (b) shows that there is an output when either input is a 'one' level and when bother outputs are a 'one' level. However, the addition operation causes the output for 'one' at both
10   inputs to be twice the intensity as that when only one input is 'one'.

The optical output for the NOR gate is shown in figure 5(a). Note that the output is the inverse of the OR operation seen at the combined signal out of the first x-coupler, figure 4, that is it goes low when the combined signal, figure 4, goes high and vise versa. Also, when both inputs are present in figure
15   4, figure 5(a) shows a decrease in the peak intensity of the spike immediately following. This represents undesired data dependent behavior and increases the bit error rate of the data. The overlayed figure of ones and zeros is shown in the eye diagram, figure 5(b). Note that a clear region vertically in the center of

the eye ensures that the 'one' and 'zero' levels can be distinguished from each other in the receiver. The horizontal space in the center of the eye ensures that a bit is not confused with its neighbor in the receiver, which would result in intersymbol interference.

5   Figure 6(a) shows the output spectrum, the two input frequencies, reduced in intensity by the filter, and the output frequency are seen.

When an optical pulse passes through an SOA, carriers are withdrawn from the conduction band to be converted into photons for amplification. The carrier reservoir is stocked by electrons from the continuous wave bias current.

10   The data dependent behavior mentioned earlier, occurring when both signals have ones simultaneously, shows up in the carrier density for the SOA shown in figure 6(b). For a 'one' level occurring on the both inputs, it can be seen that the carriers are drawn down more than for a 'one' at only one of the inputs.

4.   NXOR GATE USING SOAS IN CROSS-GAIN MODULATION

15   Figure 7 shows a schematic for the NXOR (not exclusive OR) gate using an SOA in cross gain modulation. The difference between the NOR and NXOR gates is that in the latter, the output when both signals are present is the same as when neither signal is present, (see earlier truth table). This is accomplished

by destructive interference (phase cancellation) between the two signals when bother signals have a 'one' level. For destructive interference to occur, the signals must have the exact same frequency and 180 degree phase difference. This is accomplished by shifting both input signals onto the same light

5 frequency at 193 THz with a common 193 THz probe laser, figure 7. A phase shifter provides a 180 degree phase difference between inputs to allow phase cancellation of simultaneous 'one' levels on both inputs. The inputs at C and D in figure 7 are shown in figure 8(a) and (b) respectively.

The combined input entering the leftmost bandpass filter in figure 7 is

10 shown in figure 9. Note that both inputs have a one pulse midway between 3 and 4 ns and that these cancel each other by destructive interference to give zero in the combined signal. The operation performed is an exclusive OR, XOR.

The XOR signal enters the third SOA operating in cross-gain mode with a probe laser at 193.2 THz. The SOA performs the inversion or NOT operation.

15 The resulting optical output for the NXOR gate is shown in figure 10(a). Note that the output is the inverse of the XOR operation seen at the combined signal, figure 9, that is it goes low when the combined signal, figure 9, goes high and vice versa. The overlayed figure of ones and zeros is shown in the

corresponding eye diagram, figure 10(b). Note that, as for the NOR gate, a clear region in the center of the eye ensures that in the receiver, the one and zero levels can be distinguished from each other and that a bit is not confused with its neighbors.

5   Figure 11(a) shows the output spectrum after the SOA and band pass filter at 193.2 THz. The 193 THz common input frequency is suppressed in favor of the probe 193.2 THz onto which the signal has been transferred. Figure 11(b) shows the carrier density for the SOA. Unlike the case of the NOR gate there is little sign of data dependent behavior.

CONCLUSION

We showed with computer simulation that a semiconductor optical amplifier (SOA) can be used in cross gain modulation as an all-optical NOR gate with return to zero (RZ) input bit streams at 2.5 Gbps. All-optical meaning that the control is optical as well as the data path. A second SOA could be used to invert the signal to produce an all-optical OR gate. We also showed that a multiplexer can perform an all-optical logic XOR. This functions by using destructive phase interference when both input signals have a one level simultaneously. An SOA following the multiplexer performs a NOT operation to give an all-optical not-exclusive-OR or NXOR gate. However, in this case both signals must come from the same laser or be synchronized with each other in frequency and phase. The optical phase difference between inputs is set to 180 degrees. Frequenctly, in logic, the inputs come form difference laser sources. In this case two SOAs are used to convert the signal modulation from the input frequencies to the same laser source by using a common probe laser. The proposed all-optical logic gates can be used to construct more complex logic systems and can be made to operate at higher bit rates.

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